

JUN 23 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Satwant Singh et al.  
Title: DELAY-MATCHED DC ASIC CONVERSION OF A PROGRAMMABLE LOGIC DEVICES  
Application No.: 10/660,814 Filing Date: September 12, 2003  
Examiner: Daniel D. Chang Group Art Unit: 2819  
Docket No.: M-15198 US Confirmation No. 2819

Irvine, California  
June 23, 2005Via Facsimile to (703) 872-9306


Mail Stop AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that the following documents are being facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below:

- 1) Response to Notice of Non-Compliant Amendment;

Dated: June 23, 2005

  
Sandra L. CarrNumber of pages (including this sheet): 11

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Satwant Singh; Cyrus Tsui  
Assignee: Lattice Semiconductor Corporation  
Title: Delay-Matched ASIC Conversion of a Programmable Logic Devices  
Serial No.: 10/660,814 Filing Date: September 12, 2003  
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June 22, 2005

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT**  
**(37 CFR 1.121)**

Dear Sir:

In response to the Notice of Non-Compliant Amendment (37 CFR 1.121) mailed May 25, 2005, Applicant resubmits the following corrected "Amendments to the Claims" section of the Amendment filed May 16, 2005 as required. A copy of the Notice is also included with this response.

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-1- Serial No. 10/660,814